

REMARKS

Claims 1-27 are pending in this application, with claims 1, 5, 9, 13, 18 and 23 being independent. Claims 1-3, 5-7, 9-11, 14-16, 19-21 and 24-26 have been amended. In particular, independent claims 1, 5 and 9 have been amended to address an antecedent basis issue, and claims 2, 3, 6, 7, 10, 11, 14-16, 19-21 and 24-26 have been amended to replace the term "any one of claims" with the term "claim." Applicants request entry of these amendments as they are formalistic in nature and, therefore, are not believed to raise any new substantive issues that would require further search and consideration by the Examiner. No new matter has been added.

Claims 6, 7, 10, 11, 14-16, 19-21 and 24-26 have been objected to for informalities. Applicants have amended the claims per the Examiner's suggestion and, therefore, request reconsideration and withdrawal of the objection to claims 6, 7, 10, 11, 14-16, 19-21 and 24-26.

Applicants acknowledge with appreciation the Examiner's indication that claims 1-12 and 18-27 are directed to allowable subject matter.

Independent claim 13 and its dependent claims 14-16 have been rejected as being anticipated by Yamazaki (U.S. Patent No. 5,644,147). Dependent claim 17, which depends from claim 13, has been rejected as being unpatentable over Yamazaki. Applicants respectfully traverse these rejections.

Independent claim 13, as amended previously, recites a semiconductor device that includes, among other features, a first electrode, a gate wiring, an insulating film, and a second electrode, "wherein said gate wiring and said second electrode are provided on said same insulating film, and wherein said first electrode is overlapped at 70% or more of an area thereof with said second electrode" (emphasis added). Applicants request reconsideration and withdrawal of the rejection of claim 13 and its dependent claims because Yamazaki does not describe or suggest: (1) the recited gate wiring and second electrode that are provided on the same insulating film; and (2) the recited first electrode that has 70% or more of its area overlapped with the second electrode.

Yamazaki describes an electro-optical device that includes: a gate line 208/222, which the Examiner equates to the recited gate wiring; a gate electrode 202, which the Examiner equates to the recited first electrode; and a pixel electrode 218, which the Examiner equates to the recited second electrode. As shown in the marked-up Fig. 3I of Yamazaki attached to this

response, the gate line 208/222 and the pixel electrode 218 are not provided on the same insulating film. Rather, the gate line 208/222 is provided on a silicon oxide film 206, and the pixel electrode 218 is provided on a silicon nitride film 217. Moreover, as shown in the attached marked-up Fig. 3I and also shown in the attached marked-up Fig. 5A of Yamazaki, the gate electrode 202 does not have 70% or more of its area overlapped with the pixel electrode 218. Rather, the gate electrode 202 and the pixel electrode 218 do not overlap at all.

For at least these reasons, applicants request reconsideration and withdrawal of the rejections of claim 13 and its dependent claims 14-17.

Applicants submit that all claims are in condition for allowance.

No fees are believed due. Please apply any other charges or credits to deposit account 06-1050.

Date: _____

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Respectfully submitted,



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